Symphony-Board

Doc rev 1.0

VAR-SOM-MX93

- USB Type Micro-AB ← USB → USB to UART ← UART# Debug → UART1
- Ext. Header ← UART#A, #B, #BT → UART5 UART6 UART7
- FFC/FPC ← Resistive Touch → Resistive Touch/ SPI8
- Ext. Header
- Ext. Header ← SPI#A → SPI6
- Ext. Header ← I2C#A, #B, #C → I2C5 I2C7 I2C1
- FFC/FPC ← I2C#C Cap. Touch
- RTC BAT → RTC ← I2C#C
- Stereo Jack ← Line In → Audio Codec/ SAI1
- Stereo Jack ← Headphones
- DMIC → CLK+DATA
- Ext. Header
- Ext. Header ← SAI#A → SAI3
- Ext. Header ← CAN → CAN Transceiver ← FLEXCAN#A → FLEXCAN1
- GPIO Expander
- Buttons/LEDs ← Boot, Reset → System Ctrl

- POWER ← 3.3V ← DC-DC
- POWER ← 5V ← DC-DC
- DC-DC, DC-DC ← 12V ← 12V DC Power Jack
- LVDS/DSI → LVDS#A (4 lanes) → Ext. Header
- DSI/LVDS → LVDS#B (4 lanes) → Ext. Header
- PMIC Control ETH Interrupt → PMIC_STDBY_REQ/PMIC_ON_REQ/ONOFF/ETH_INT2 → Ext. Header
- MIPI CSI2 → MIPI CSI (Dual Lane) → Edge connector
- Boot Mode WDOG → Boot Mode\WDOG → Edge connector
- ADC In CLK In Tamper IN → ADC/CLK/Tamper → Edge connector
- Gigabit Ethernet PHY → ETH#A+LEDs → RJ45
- ENET1 RGMII → RGMII → Ethernet PHY → ETH1+LEDs → RJ45
- ENET1 RGMII → Ext. Header
- PCIe Clock Generator → Mini-PCIe Socket
- Wi-Fi control PMIC Interrupt → PCIe#A → Mini-PCIe Socket
- USB#2 2.0 OTG → USB#A Host → USB Type-A
- USB#1 2.0 OTG → USB#B OTG → USB Type-C
- BT PCM → BT PCM → USB Type-C
- uSDHC2 → uSDHC#A → uSD
- TPM3 CH3 TPM4 CH3 TPM6 CH3 → PWM#A,#B, #C → Ext. Header
- GPIOs → GPIOs → Ext. Header

Pin-compatible with other modules from the VAR-SOM Pin2Pin family

No pin2pin compatibility